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APPLICATION NO.	FILI	NG DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/781,241	10/781,241 02/17/2004		Ali Keshavarzi	42P6184C	2359
8791	7590	02/24/2005		EXAMINER	
BLAKELY 12400 WILS		FF TAYLOR &	nguyen, Joseph H		
SEVENTH		2E VIIIG		ART UNIT	PAPER NUMBER
LOS ANGE	LOS ANGELES, CA 90025-1030			2815	
				DATE MAILED: 02/24/200	ς.

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/781,241						
Office Action Summary	Examiner	KESHAVARZI ET AL.  Art Unit					
•	Joseph Nguyen	2815					
The MAILING DATE of this communication app							
Period for Reply	ears on the cover sheet with the c	orrespondence address					
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	16(a). In no event, however, may a reply be tim within the statutory minimum of thirty (30) days ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).					
Status							
1) Responsive to communication(s) filed on 18 Ja	nuary 2005.						
· <u> </u>							
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
	Claim(s) <u>1-26</u> is/are pending in the application.						
4a) Of the above claim(s) <u>25 and 26</u> is/are withdrawn from consideration.							
	Claim(s) is/are allowed.						
	Claim(s) <u>1-15, 17,18 and 20-24</u> is/are rejected.						
	•						
8) Claim(s) are subject to restriction and/or	election requirement.						
Application Papers							
9) The specification is objected to by the Examine	r.						
10)⊠ The drawing(s) filed on <u>17 February 2004</u> is/are: a)⊠ accepted or b)□ objected to by the Examiner.							
Applicant may not request that any objection to the	drawing(s) be held in abeyance. See	37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.					
Priority under 35 U.S.C. § 119							
	priority under 35 H.S.C. & 119(a)	-(d) or (f)					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a)⊠ All b)□ Some * c)□ None of: 1.□ Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents		on No. 09/165 483					
3. Copies of the certified copies of the prior							
•		d III tilis Ivational Stage					
application from the International Bureau	· · · · · · · · · · · · · · · · · · ·	d					
* See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s)							
1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  Paper No(s)/Mail Date							
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)		atent Application (PTO-152)					
Paper No(s)/Mail Date <u>2/17/2004</u> .	6) Other:						

Application/Control Number: 10/781,241

Art Unit: 2815

### **DETAILED ACTION**

#### Election/Restrictions

Applicant's election without traverse of claims 1-24 in the reply filed on 1/18/2005 is acknowledged.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-2, 4, 6, 8-15, 17-18, 20-24 are rejected under 35 U.S.C. 102(b) as being anticipated by Hwang.

Regarding claim 1, Hwang discloses on figure 1g a field effect transistor comprising a substrate 10, a source 52 and a drain 54; an electric field terminal region 16 in the substrate; and a body 20 above the electric field terminal region between the source and drain, wherein there is a barrier 12 between the electric field terminal region and the body.

Regarding claim 2, Hwang discloses on figure 1g the barrier 12 is an insulator layer between the body and the electric field terminal region.

Regarding claim 4, Hwang discloses on figure 1g a field effect transistor comprising an insulator layer 12; a body 20 above the insulator layer between a source

Application/Control Number: 10/781,241

Art Unit: 2815

52 and a drain 54; a substrate 10 below the insulator layer; a gate 28 above the body and between the source and drain, the gate having a length; and an electric field terminal region 16 in the substrate.

Regarding claim 6, Hwang discloses on figure 1g the body 20 is lightly doped.

Regarding claims 8-14, the claim language is merely the intended use. It has been held that a recitation with respect to the manner in which a claimed apparatus is intended to be employed does not differentiate the claimed apparatus from a prior art apparatus satisfying the claimed structural limitation. Ex Parte Masham, 2 USPQ F.2d 1647 (1987). Therefore, the claimed invention does not structurally distinguish from Hwang herein.

Regarding claim 15, Hwang discloses on figure 1g the electric field terminal region 16 extends beneath the entire length of the gate 28.

Regarding claim 17, Hwang discloses on figure 1g the transistor is a PMOSFET.

Regarding claim 18, Hwang discloses on figure 1g the transistor is an NMOSFET.

Regarding claim 20, Hwang discloses on figure 1g a die comprising a first and second field effect transistor each including a substrate 10; an electric field terminal region 16 in the substrate, a source 52 and a drain 54; and a body 14 above the electric field terminal region between the source and drain.

Regarding claim 21, Hwang discloses on figure 1g an insulator layer 12 between the substrate and the body.

Regarding claim 22, Hwang discloses on figure 1g the insulator layer 12 is shared by the first and second field effect transistor.

Regarding claim 23, Hwang discloses on figure 1g the body 14 is shared by the first and second field effect transistors.

Regarding claim 24, Hwang discloses on figure 1g the electric field terminal region 16 is shared by the first and second field effect transistors.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 3, 5 and 7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hwang as applied to claims1 and 4 above, and further in view of Ayres et al.

Regarding claims 3 and 5, Hwang discloses on figure 1g substantially all the structure set forth in the claimed invention except the body being undoped. However, Ayres et al. discloses on figure 8 the body 111 is undoped (col. 6, lines 24-27). In view of such teaching, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Hwang by having the body being undoped for the purpose of providing a certain application to a field effect transistor.

Regarding claim 7, Hwang discloses on figure 1g substantially all the structure set forth in the claimed invention except the channel being undoped. However, Ayres et al. discloses on figure 8 the channel 111 is undoped (col. 6, lines 24-27). In view of such teaching, it would have been obvious to one of ordinary skill in the art at the time the

invention was made to modify Hwang by having the channel being undoped for the purpose of providing a certain application to a field effect transistor.

Note that the body 111 in figure 8 of Ayres et al. forms a channel between the source and drain as a voltage is applied to the gate.

# Allowable Subject Matter

Claims 16 and 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph Nguyen whose telephone number is (571) 272-1734. The examiner can normally be reached on Monday-Friday, 7:30 am- 4:30 pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306 for regular communications.

Application/Control Number: 10/781,241

Art Unit: 2815

Page 6

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JN February 11, 2005

ALLAN R. WILSON PRIMARY EXAMINER